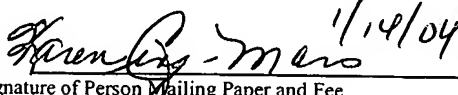


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I hereby certify that this paper and all fees are being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 CFR Sec. 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, P.O. Box 1450, ALEXANDRIA, VA, 22313-1450. on, JANUARY 14, 2004. The applicant and/or attorney requests the date of deposit as the filing date.

KAREN CINO-MARS

Name of Person Mailing Paper and Fee

 1/14/04
Signature of Person Mailing Paper and Fee

IN THE UNITED STATES PATENT AND TRADEMARK

In re application of _____ : 1/14/04
Kevin Petrarca, et al. :
serial No. Not yet assigned : Examiner: not yet assigned

Filed: 01/14/04 International Business Machines
Corporation
2070 Route 52
Hopewell Junction, NY 12533

**TITLE: ENCAPSULATED METAL STRUCTURES FOR SEMICONDUCTOR
DEVICES AND MIM CAPACITORS INCLUDING THE SAME**

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

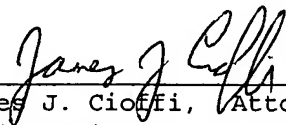
Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. 1.56, and further pursuant to the provisions of 37 C.F.R. 1.97 and 1.98, applicants hereby respectfully submit copies of the non-US patents and publications as listed on Form PTO-1449, attached hereto.

In citing these documents, no representation is made nor intended as to the pertinency or non-pertinency of the art, that better art than that listed is not available, or that other art is not applicable.

No fee is believed to be due for this submission. If any fees are required, however, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 09-0458.

Respectfully submitted,
Kevin Petrarca, et al.

By  1/13/04
James J. Cioffi, Attorney
Registration No. 51,564
Telephone No. 845-894-3363

INFORMATION DISCLOSURE CITATION*(Use several sheets if necessary)*ATTY DOCKET NO.
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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	3,464,855	9/02/069	Shaheen, et al.			
	5,549,808	8/27/96	Farooq, et al.			
	5,256,274	10/26/93	Poris			
	5,308,796	5/03/94	Feldman, et al.			
	5,198,389	3/30/93	van der Putten, et al.			
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	3,385,773	5/28/68	John J. Frantzen			
	5,793,112	8/11/98	Hasegawa, et al.			
	6,251,771	6/26/01	Smith, et al.			
	6,207,552	3/27/01	Wang, et al.			
	6,110,648	8/29/00	Jang			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

			Electroless Plating for Low-Cost High-Leverage Wiring, IBM Technical Disclosure Bulletin, Vol. 32 No. 3A, August 1989

EXAMINER

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,251,786	6/26/01	Zhou, et al.			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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